

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application

) PATENT APPLICATION

Inventor(s): Garg, et al.

Serial No.: 07/726,773

Filed: July 8, 1991

Title: RISC MICROPROCESSOR  
ARCHITECTURE IMPLEMENTING  
MULTIPLE TYPED REGISTER SETS

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name; I believe that I am the original, first and sole inventor (if one name is listed below), first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

RISC MICROPROCESSOR ARCHITECTURE  
IMPLEMENTING MULTIPLE TYPED REGISTER SETS

the specification of which (check applicable ones):

\_\_\_\_\_ is attached hereto;

  X   was filed with the above-identified "Filed" date and "Serial No."

\_\_\_\_\_ was amended on (or amended through) \_\_\_\_\_.

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment(s) referred to above. I acknowledge the duty to disclose information which is material to the examination of the application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true, and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

(1) Full name of sole  
or first inventor: Sanjiv Garg

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(1) Post Office Address: Same as Residence

(1) Citizenship: U.S.A.

(1) Inventor's signature: Sanjiv Garg

(1) Date: 9/6/91

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(2) Full name of second  
joint inventor: Derek J. Lentz

(2) Residence: 17400 Phillips Avenue  
Los Gatos, California 95032

(2) Post Office Address: Same as Residence

(2) Citizenship: U.S.A.

(2) Inventor's signature: Derek J. Lentz

(2) Date: 9/6/91

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(3) Full name of third  
joint inventor: Le Trong Nguyen

(3) Residence: 15096 Danielle Place  
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(3) Post Office Address: Same as Residence

(3) Citizenship: Vietnam

(3) Inventor's signature: Le Trong Nguyen

(3) Date: 9/6/91

- (4) Full name of fourth <sup>su</sup>  
joint inventor: Sho Long Chen
- (4) Residence: 14411 Quito Road  
Saratoga, California 95070
- (4) Post Office Address: Same as Residence
- (4) Citizenship: U.S.A.
- (4) Inventor's signature: *Sho Long Chen*
- (4) Date: Sept 6, 91

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Title 37, Code of Federal Regulations, §1.56(a)

SECTION 1.56. DUTY OF DISCLOSURE; FRAUD; STRIKING  
OR REJECTION OF APPLICATION.

(a) A duty of candor and good faith toward the Patent and Trademark Office rests on the inventor, on each attorney or agent who prepares or prosecutes the application and on every other individual who is substantively involved in the preparation or prosecution of the application and who is associated with the inventor, with the assignee or with anyone to whom there is an obligation to assign the application. All such individuals have a duty to disclose to the Office information they are aware of which is material to the examination of the application. Such information is material when there is a substantial likelihood that a reasonable examiner would consider it important in deciding whether to allow the application to issue as a patent. The duty is commensurate with the degree of involvement in the preparation or prosecution of the application.